

# PM1200 CPU Module -- FIR

(Showing PM2510-4 Stuff Option)

## Notes:

See design and layout  
notes on schematic.

## Revision History of Cedar:

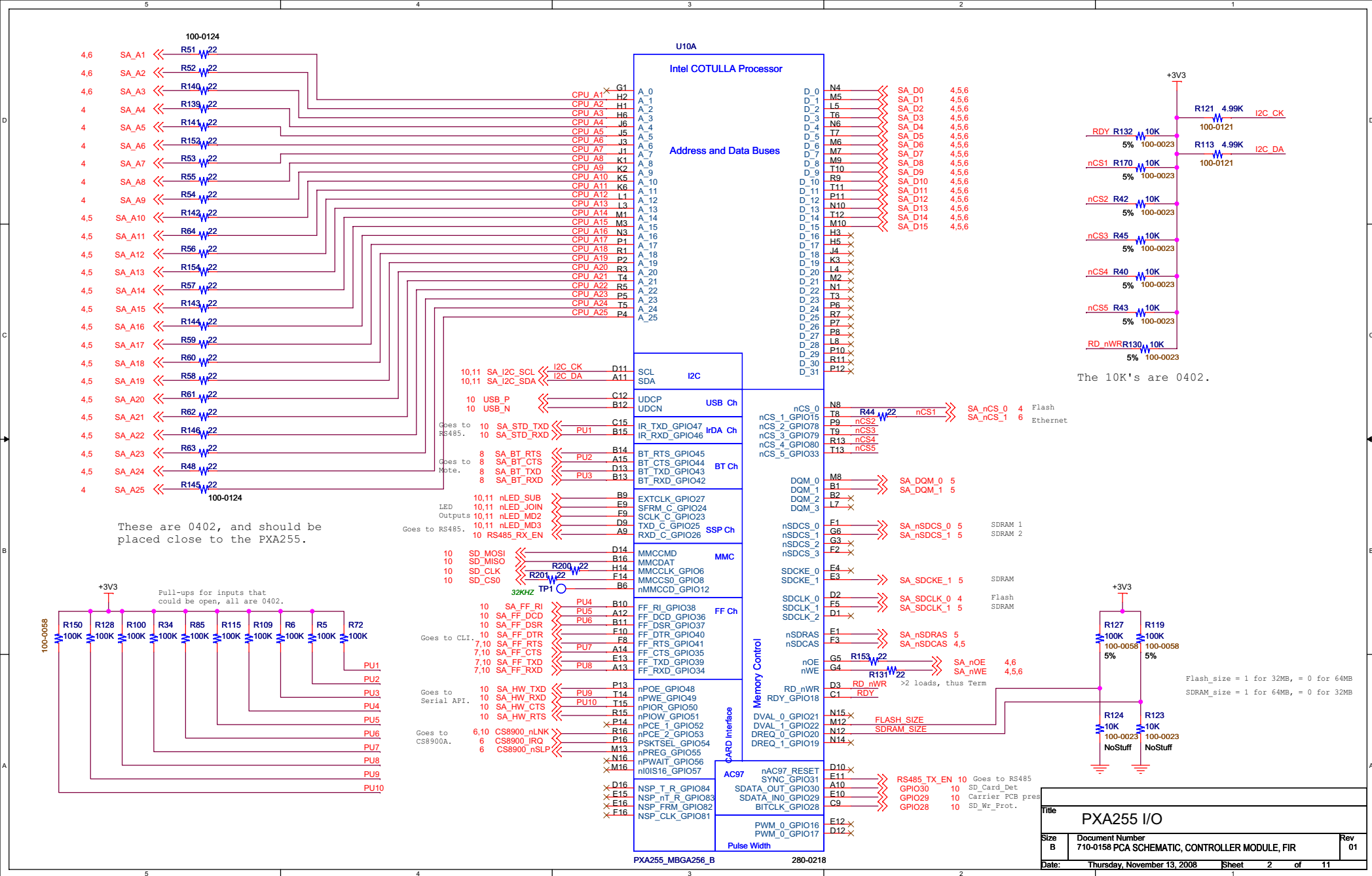
Rev	Description	ECO	Author
01	Initial release.	ECO 660	S Bennett

PCB Fab: 600-0112  
PCA BOM: 700-0158  
PCA ASSY: 705-0158

Title		
Title Page / Revision History		
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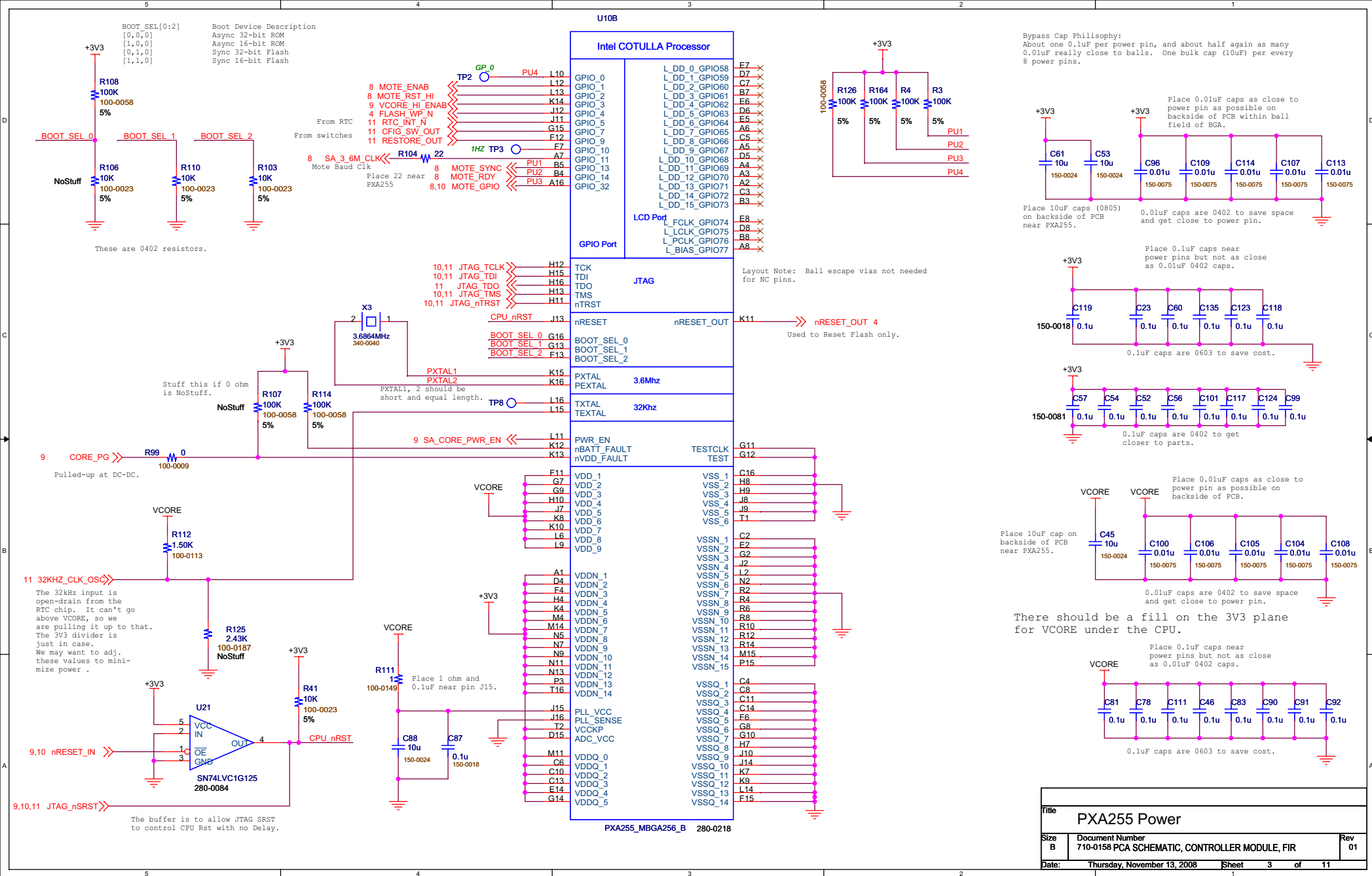
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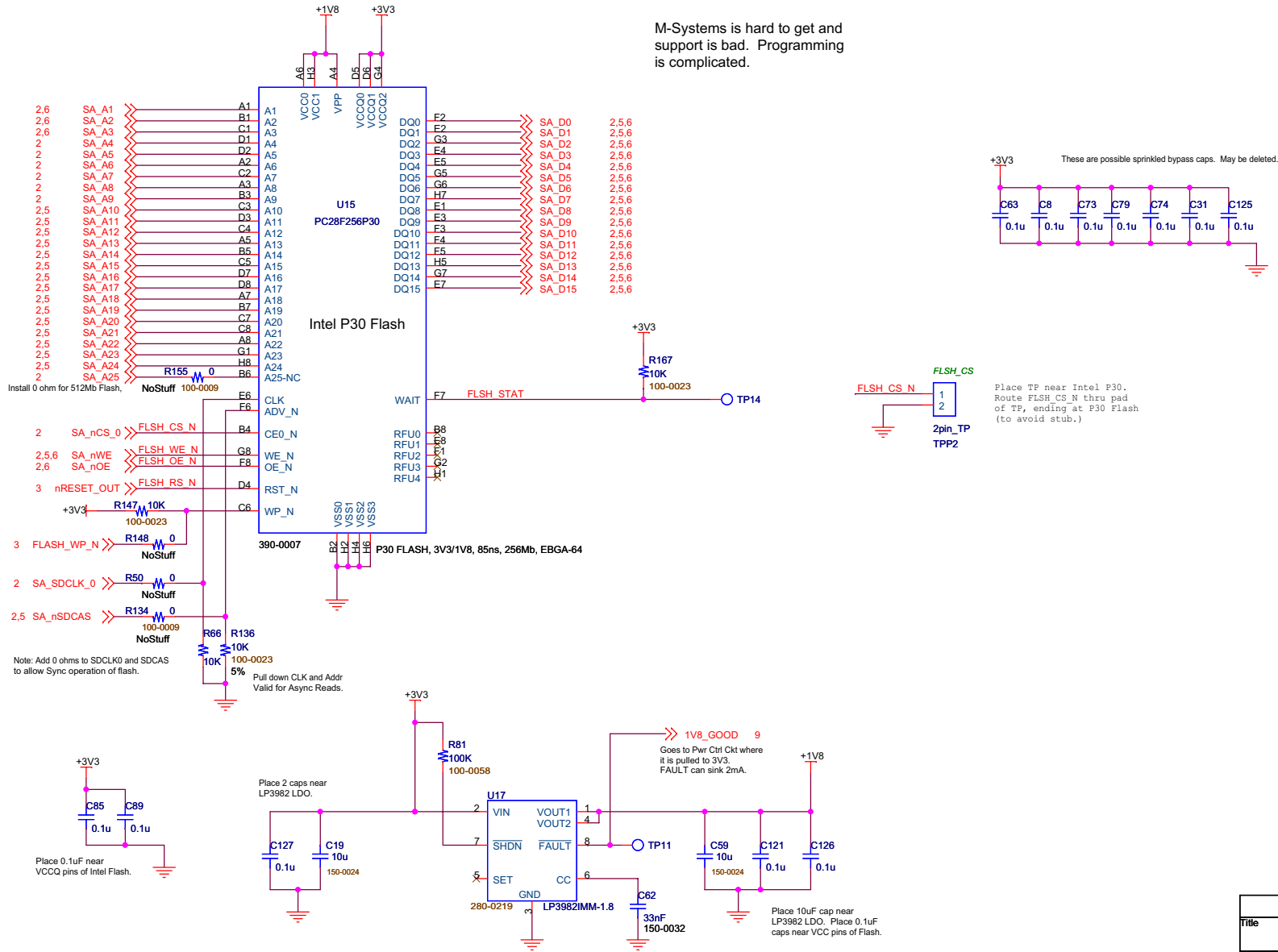


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P30 Flash with PXA255 gets discount.  
Thus, P30 will be cheaper than Spansion.

M-Systems is hard to get and  
support is bad. Programming  
is complicated.



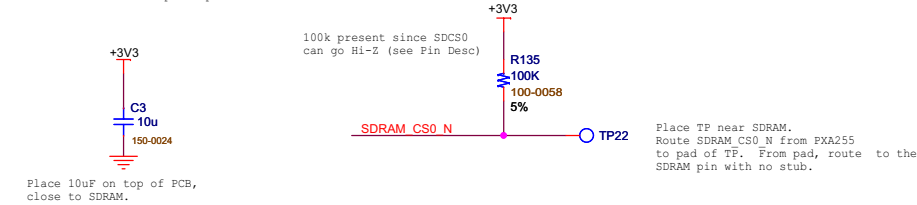
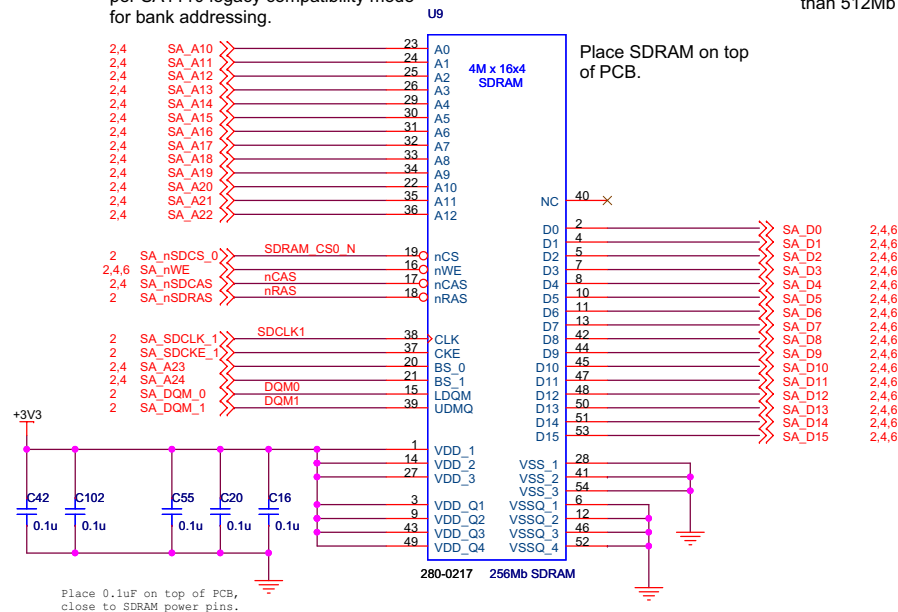
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Flash		
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Note that addressing is connected per SA1110 legacy compatibility mode for bank addressing.

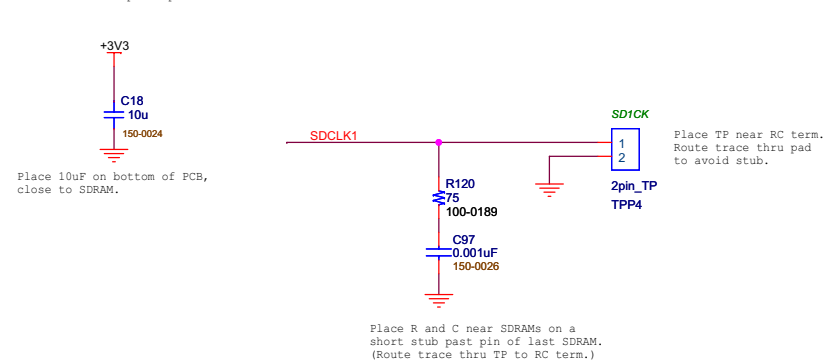
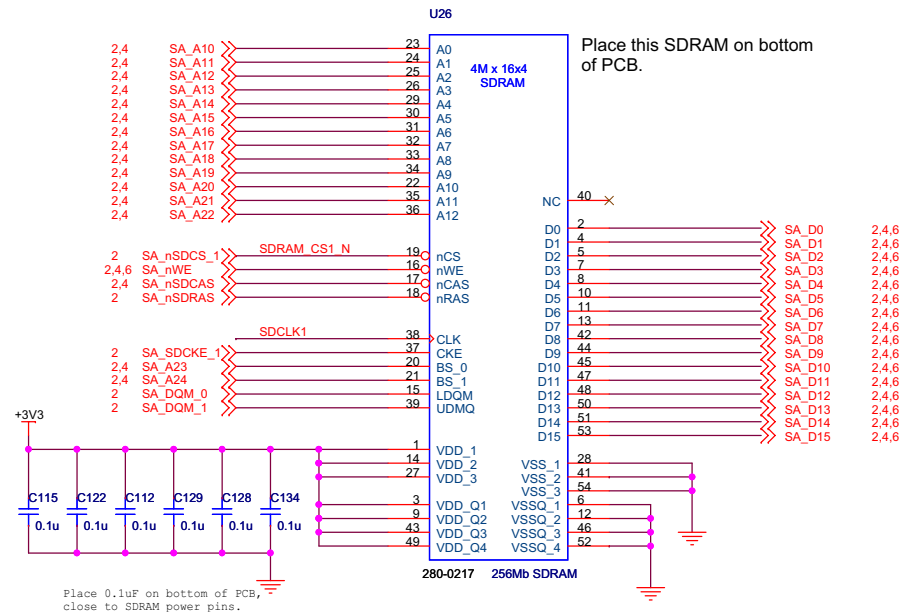
256Mb parts are more cost effective (by 50%) than 512Mb parts.



Note: Both SDRAM are loaded for 64MB. It is 2 x 256Mb for 64MB of x16 RAM. If we only need 32MB, then the one of the right is NOSTUFF, and we have 32MB of x16 SDRAM. SDRAM on left is Partition 0. SDRAM on right is Partition 1.

Note: For 0-70C, Samsung SDRAMs are more cost effective (about \$5 instead of \$7).

Place this SDRAM on bottom of PCB.



Place R and C near SDRAMs on a short stub past pin of last SDRAM. (Route trace thru TP to RC term.)

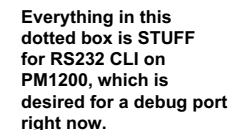
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SDRAM			
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2,10 SA\_FF\_TXD >> CLI\_TXD  
2,10 SA\_FF\_RXD << CLI\_RXD  
2,10 SA\_FF\_RTS >> CLI\_RTS  
2,10 SA\_FF\_CTS << CLI\_CTS

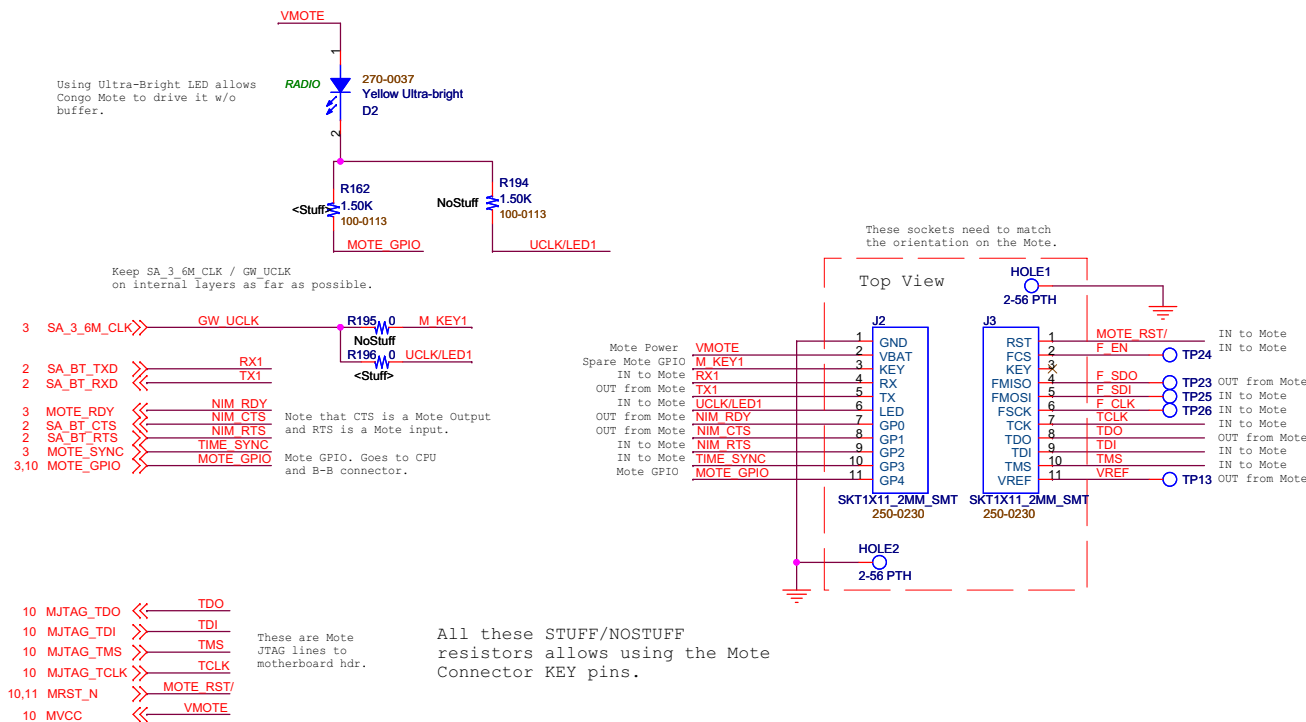


Note: 5-pin header allows RS-232 connection to CLI port. This would be stuffed for debug and development only. Header is unshrouded and unkeyed, but plugging in backwards causes no failures.

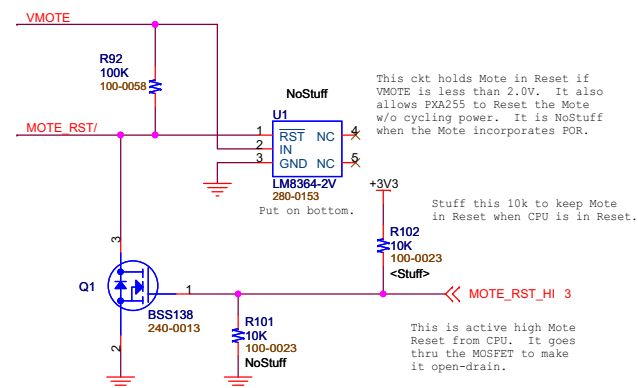
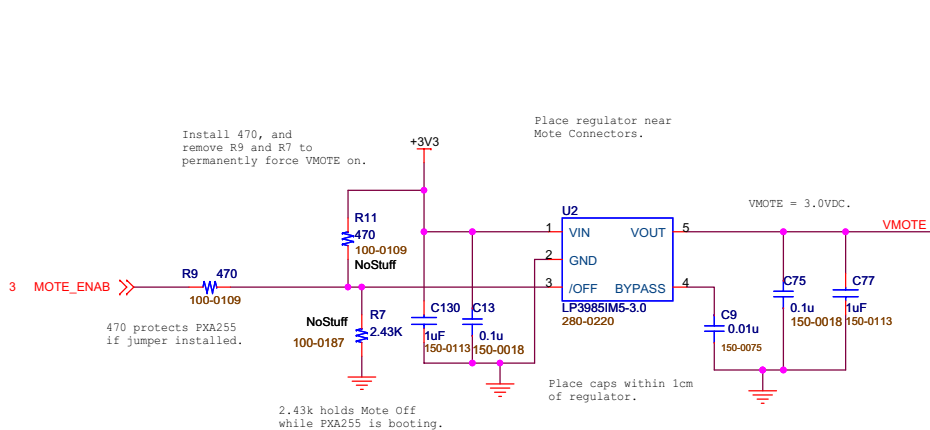
NOSTUFF this CLI LVTTTL test port.

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RS-232 Ports				
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All these STUFF/NOSTUFF resistors allows using the Mote Connector KEY pins.



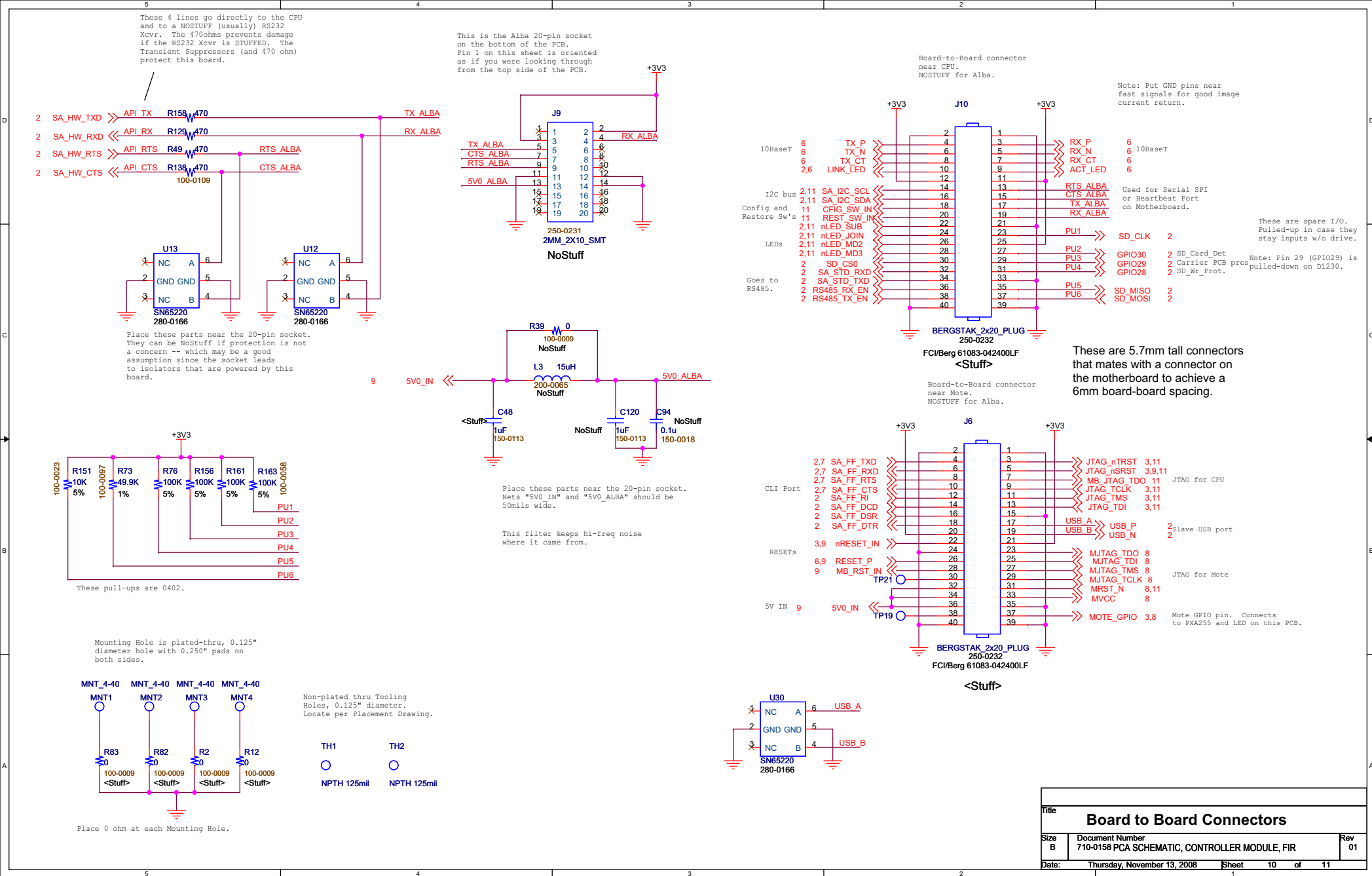
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Mote Interface			
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